

# **μA7800 SERIES**

## **THREE - TERMINAL POSITIVE VOLTAGE REGULATORS**

### **FAIRCHILD LINEAR INTEGRATED CIRCUITS**

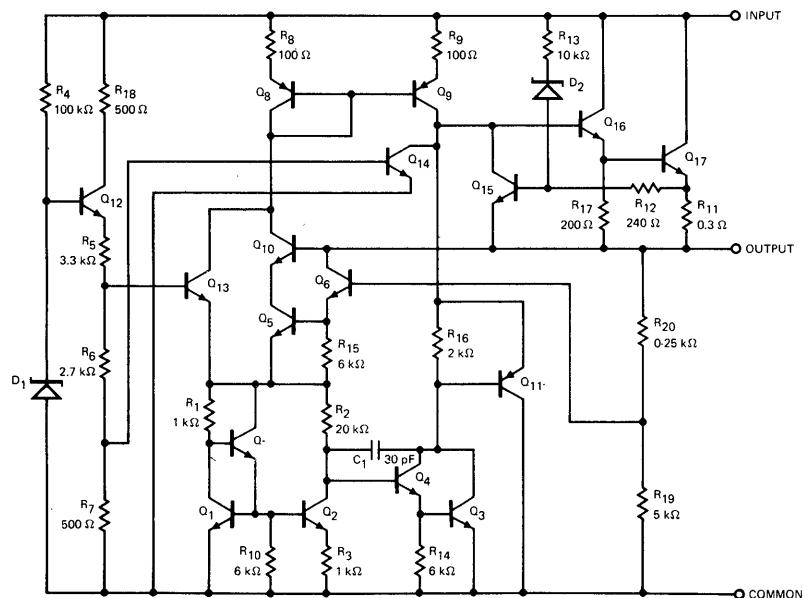
**GENERAL DESCRIPTION** — The μA7800 series of Three-Terminal Positive Voltage Regulators are constructed using the Fairchild Planar\* epitaxial process. These regulators employ internal current limiting, thermal shutdown and safe-area compensation making them essentially blow-out proof. If adequate heat sinking is provided, they can deliver over 1A output current. They are intended as fixed-voltage regulators in a wide range of applications including local, on-card regulation for elimination of noise and distribution problems associated with single point regulation. In addition to use as fixed voltage regulators, these devices can be used with external components to obtain adjustable output voltages and currents and as the power pass element in precision regulators.

- OUTPUT CURRENT IN EXCESS OF 1 AMP
- NO EXTERNAL COMPONENTS
- INTERNAL THERMAL OVERLOAD PROTECTION
- INTERNAL SHORT CIRCUIT CURRENT LIMITING
- OUTPUT TRANSISTOR SAFE-AREA COMPENSATION
- AVAILABLE IN THE PLASTIC TO-220 AND THE METAL TO-3 PACKAGE

#### **ABSOLUTE MAXIMUM RATINGS**

Input Voltage (5 V through 18 V)	35 V
(24 V)	40 V
Internal Power Dissipation (Note 1)	Internally Limited
Storage Temperature Range	-65°C to +150°C
Operating Junction Temperature Range	0°C to +125°C
Lead Temperature (Soldering, 60 second time limit) TO-3 Package	300°C
(Soldering, 10 second time limit) TO-220 Package	230°C

#### **EQUIVALENT CIRCUIT**

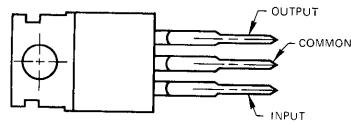


Note on following page.

#### **VOLTAGE RANGE**

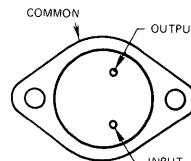
μA7805 . . . . .	5 V
μA7806 . . . . .	6 V
μA7808 . . . . .	8 V
μA7812 . . . . .	12 V
μA7815 . . . . .	15 V
μA7818 . . . . .	18 V
μA7824 . . . . .	24 V

#### **CONNECTION DIAGRAMS** **TO-220 PLASTIC POWER PACKAGE** (TOP VIEW)



**ORDER PART NOS:** UGH7805393  
UGH7806393  
UGH7808393  
UGH7812393  
UGH7815393  
UGH7818393  
UGH7824393

#### **TO-3 PACKAGE** (TOP VIEW)



**ORDER PART NOS:** UGJ7805393  
UGJ7806393  
UGJ7808393  
UGJ7812393  
UGJ7815393  
UGJ7818393  
UGJ7824393

\*Planar is a patented Fairchild process.

# FAIRCHILD LINEAR INTEGRATED CIRCUITS • μA7800 SERIES

## μA7805

**ELECTRICAL CHARACTERISTICS** ( $V_{IN} = 10\text{ V}$ ,  $I_{OUT} = 500\text{ mA}$ ,  $0^\circ\text{C} < T_J < 125^\circ\text{C}$ , unless otherwise specified)

PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Output Voltage	$T_J = 25^\circ\text{C}$	4.8	5.0	5.2	V
Line Regulation	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 100\text{ mA}$ $7\text{ V} \leq V_{IN} \leq 25\text{ V}$ $8\text{ V} \leq V_{IN} \leq 12\text{ V}$ $T_J = 25^\circ\text{C}$ , $I_{OUT} = 500\text{ mA}$ $7\text{ V} \leq V_{IN} \leq 25\text{ V}$ $8\text{ V} \leq V_{IN} \leq 12\text{ V}$		7.0 2.0 35 8.0	50 25 100 50	mV mV mV mV
Load Regulation	$T_J = 25^\circ\text{C}$ , $5\text{ mA} \leq I_{OUT} \leq 1.5\text{ A}$ $250\text{ mA} \leq I_{OUT} \leq 750\text{ mA}$		11 4.0	100 50	mV mV
Output Voltage	$7\text{ V} \leq V_{IN} \leq 20\text{ V}$ , $5\text{ mA} \leq I_{OUT} \leq 1.0\text{ A}$ $p \leq 15\text{ W}$	4.75		5.25	V
Quiescent Current	$T_J = 25^\circ\text{C}$		4.3	8.0	mA
Quiescent Current Change	$7\text{ V} \leq V_{IN} \leq 25\text{ V}$ $5\text{ mA} \leq I_{OUT} \leq 1.5\text{ A}$			1.3 0.5	mA mA
Output Noise Voltage	$T_A = 25^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ kHz}$		40		μV
Long Term Stability				20	mV
Ripple Rejection	$I_{OUT} = 20\text{ mA}$ , $f = 120\text{ Hz}$		70		dB
Dropout Voltage	$I_{OUT} = 1\text{ A}$ , $T_J = 25^\circ\text{C}$		2.0		V
Output Resistance				30	mΩ
Short Circuit Current Limit	$T_J = 25^\circ\text{C}$		750		mA
Average Temperature Coefficient of Output Voltage	$I_{OUT} = 5\text{ mA}$ , $0^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$			-1.3	mV/°C

## μA7806

**ELECTRICAL CHARACTERISTICS** ( $V_{IN} = 11\text{ V}$ ,  $I_{OUT} = 500\text{ mA}$ ,  $0^\circ\text{C} < T_J < 125^\circ\text{C}$ , unless otherwise specified)

PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Output Voltage	$T_J = 25^\circ\text{C}$	5.75	6.0	6.25	V
Line Regulation	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 100\text{ mA}$ $8\text{ V} \leq V_{IN} \leq 25\text{ V}$ $9\text{ V} \leq V_{IN} \leq 13\text{ V}$ $T_J = 25^\circ\text{C}$ , $I_{OUT} = 500\text{ mA}$ $8\text{ V} \leq V_{IN} \leq 25\text{ V}$ $9\text{ V} \leq V_{IN} \leq 13\text{ V}$		9.0 3.0 43 10	60 30 120 60	mV mV mV mV
Load Regulation	$T_J = 25^\circ\text{C}$ , $5\text{ mA} \leq I_{OUT} \leq 1.5\text{ A}$ $250\text{ mA} \leq I_{OUT} \leq 750\text{ mA}$		13	120	mV
Output Voltage	$8\text{ V} \leq V_{IN} \leq 21\text{ V}$ , $5\text{ mA} \leq I_{OUT} \leq 1.0\text{ A}$ $p \leq 15\text{ W}$	5.7		6.3	V
Quiescent Current	$T_J = 25^\circ\text{C}$		4.3	8.0	mA
Quiescent Current Change	$8\text{ V} \leq V_{IN} \leq 25\text{ V}$ $5\text{ mA} \leq I_{OUT} \leq 1.5\text{ A}$			1.3 0.5	mA mA
Output Noise Voltage	$T_A = 25^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ kHz}$		45		μV
Long Term Stability				24	mV
Ripple Rejection	$I_{OUT} = 20\text{ mA}$ , $f = 120\text{ Hz}$		65		dB
Dropout Voltage	$I_{OUT} = 1\text{ A}$ , $T_J = 25^\circ\text{C}$		2.0		V
Output Resistance	$I_{OUT} = 500\text{ mA}$		35		mΩ
Short Circuit Current Limit	$T_J = 25^\circ\text{C}$		550		mA
Average Temperature Coefficient of Output Voltage	$I_{OUT} = 5\text{ mA}$ , $0^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$		-1.0		mV/°C

NOTE 1. Thermal resistance without a heat sink for junction to case temperature is  $4.0^\circ\text{C/W}$  for TO-3 package,  $2.0^\circ\text{C/W}$  for TO-220 package; ambient to case temperature is  $35^\circ\text{C/W}$  for TO-3 package and  $50^\circ\text{C/W}$  for TO-220 package.

# FAIRCHILD LINEAR INTEGRATED CIRCUITS • μA7800 SERIES

## μA7808

**ELECTRICAL CHARACTERISTICS (V<sub>IN</sub> = 14 V, I<sub>OUT</sub> = 500 mA, 0°C < T<sub>J</sub> < 125°C, unless otherwise specified)**

PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Output Voltage	T <sub>J</sub> = 25°C	7.7	8.0	8.3	V
Line Regulation	T <sub>J</sub> = 25°C, I <sub>OUT</sub> = 100 mA 10.5 V ≤ V <sub>IN</sub> ≤ 25 V 11 V ≤ V <sub>IN</sub> ≤ 17 V T <sub>J</sub> = 25°C, I <sub>OUT</sub> = 500 mA 10.5 V ≤ V <sub>IN</sub> ≤ 25 V 11 V ≤ V <sub>IN</sub> ≤ 17 V		12 5.0 50 22	80 40 160 80	mV mV mV mV
Load Regulation	T <sub>J</sub> = 25°C, 5 mA ≤ I <sub>OUT</sub> ≤ 1.5 A 250 mA ≤ I <sub>OUT</sub> ≤ 750 mA		26	160	mV
Output Voltage	10.5 V ≤ V <sub>IN</sub> ≤ 23 V, 5 mA ≤ I <sub>OUT</sub> ≤ 1.0 A, P ≤ 15 W	7.6		8.4	V
Quiescent Current	T <sub>J</sub> = 25°C		4.3	8.0	mA
Quiescent Current Change	10.5 V ≤ V <sub>IN</sub> ≤ 25 V 5 mA ≤ I <sub>OUT</sub> ≤ 1.5 A			1.0 0.5	mA mA
Output Noise Voltage	T <sub>A</sub> = 25°C, 10 Hz ≤ f ≤ 100 kHz		52		μV
Long Term Stability				32	mV
Ripple Rejection	I <sub>OUT</sub> = 20 mA, f = 120 Hz		62		dB
Dropout Voltage	I <sub>OUT</sub> = 1 A, T <sub>J</sub> = 25°C		2.0		V
Output Resistance	I <sub>OUT</sub> = 500 mA		40		mΩ
Short Circuit Current Limit	T <sub>J</sub> = 25°C		450		mA
Average Temperature Coefficient of Output Voltage	I <sub>OUT</sub> = 5 mA, 0°C ≤ T <sub>A</sub> ≤ 125°C		-1.0		mV/°C

## μA7812

**ELECTRICAL CHARACTERISTICS (V<sub>IN</sub> = 19 V, I<sub>OUT</sub> = 500 mA, 0°C < T<sub>J</sub> < 125°C, unless otherwise specified)**

PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Output Voltage	T <sub>J</sub> = 25°C	11.5	12.0	12.5	V
Line Regulation	T <sub>J</sub> = 25°C, I <sub>OUT</sub> = 100 mA 14.5 V ≤ V <sub>IN</sub> ≤ 30 V 16 V ≤ V <sub>IN</sub> ≤ 22 V T <sub>J</sub> = 25°C, I <sub>OUT</sub> = 500 mA 14.5 V ≤ V <sub>IN</sub> ≤ 30 V 16 V ≤ V <sub>IN</sub> ≤ 22 V		13 6.0 55 24	120 60 240 120	mV mV mV mV
Load Regulation	T <sub>J</sub> = 25°C, 5 mA ≤ I <sub>OUT</sub> ≤ 1.5 A 250 mA ≤ I <sub>OUT</sub> ≤ 750 mA		46	240	mV
Output Voltage	14.5 V ≤ V <sub>IN</sub> ≤ 27 V, 5 mA ≤ I <sub>OUT</sub> ≤ 1.0 A, P ≤ 15 W			12.6	V
Quiescent Current	T <sub>J</sub> = 25°C		4.4	8.0	mA
Quiescent Current Change	14.5 V ≤ V <sub>IN</sub> ≤ 30 V 5 mA ≤ I <sub>OUT</sub> ≤ 1.5 A			1.0 0.5	mA mA
Output Noise Voltage	T <sub>A</sub> = 25°C, 10 Hz ≤ f ≤ 100 kHz		75		μV
Long Term Stability				48	mV
Ripple Rejection	I <sub>OUT</sub> = 20 mA, f = 120 Hz		61		dB
Dropout Voltage	I <sub>OUT</sub> = 1 A, T <sub>J</sub> = 25°C		2.0		V
Output Resistance	I <sub>OUT</sub> = 500 mA		75		mΩ
Short Circuit Current Limit	T <sub>J</sub> = 25°C		350		mA
Average Temperature Coefficient of Output Voltage	I <sub>OUT</sub> = 5 mA, 0°C ≤ T <sub>A</sub> ≤ 125°C		-2.0		mV/°C

**FAIRCHILD LINEAR INTEGRATED CIRCUITS • μA7800 SERIES**

**μA7815**

**ELECTRICAL CHARACTERISTICS ( $V_{IN} = 23\text{ V}$ ,  $I_{OUT} = 500\text{ mA}$ ,  $0^\circ\text{C} < T_J < 125^\circ\text{C}$ , unless otherwise specified)**

PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Output Voltage	$T_J = 25^\circ\text{C}$	14.4	15.0	15.6	V
Line Regulation	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 100\text{ mA}$ $17.5\text{ V} \leq V_{IN} \leq 30\text{ V}$ $20\text{ V} \leq V_{IN} \leq 26\text{ V}$	14	150	150	mV
	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 500\text{ mA}$ $17.5\text{ V} \leq V_{IN} \leq 30\text{ V}$ $20\text{ V} \leq V_{IN} \leq 26\text{ V}$	6.0	75	75	mV
Load Regulation	$T_J = 25^\circ\text{C}$ , $5\text{ mA} \leq I_{OUT} \leq 1.5\text{ A}$ $250\text{ mA} \leq I_{OUT} \leq 750\text{ mA}$	68	300	300	mV
Output Voltage	$17.5\text{ V} \leq V_{IN} \leq 30\text{ V}$ , $5\text{ mA} \leq I_{OUT} \leq 1.0\text{ A}$ , $p \leq 15\text{ W}$	14.25	15.75	15.75	V
Quiescent Current	$T_J = 25^\circ\text{C}$	4.4	8.0	8.0	mA
Quiescent Current Change	$17.5\text{ V} \leq V_{IN} \leq 30\text{ V}$ $5\text{ mA} \leq I_{OUT} \leq 1.5\text{ A}$	1.0	0.5	0.5	mA
Output Noise Voltage	$T_A = 25^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ kHz}$	90			μV
Long Term Stability				60	mV
Ripple Rejection	$I_{OUT} = 20\text{ mA}$ , $f = 120\text{ Hz}$	60			dB
Dropout Voltage	$I_{OUT} = 1\text{ A}$ , $T_J = 25^\circ\text{C}$	2.0			V
Output Resistance	$I_{OUT} = 500\text{ mA}$	95			mΩ
Short Circuit Current Limit	$T_J = 25^\circ\text{C}$	230			mA
Average Temperature Coefficient of Output Voltage	$I_{OUT} = 5\text{ mA}$ , $0^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$	-2.0			mV/°C

**μA7818**

**ELECTRICAL CHARACTERISTICS ( $V_{IN} = 27\text{ V}$ ,  $I_{OUT} = 500\text{ mA}$ ,  $0^\circ\text{C} < T_J < 125^\circ\text{C}$ , unless otherwise specified)**

PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Output Voltage	$T_J = 25^\circ\text{C}$	17.3	18.0	18.7	V
Line Regulation	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 100\text{ mA}$ $21\text{ V} \leq V_{IN} \leq 33\text{ V}$ $24\text{ V} \leq V_{IN} \leq 30\text{ V}$	25	180	180	mV
	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 500\text{ mA}$ $21\text{ V} \leq V_{IN} \leq 33\text{ V}$ $24\text{ V} \leq V_{IN} \leq 30\text{ V}$	10	90	90	mV
Load Regulation	$T_J = 25^\circ\text{C}$ , $5\text{ mA} \leq I_{OUT} \leq 1.0\text{ A}$ $250\text{ mA} \leq I_{OUT} \leq 750\text{ mA}$	110	360	360	mV
Output Voltage	$21\text{ V} \leq V_{IN} \leq 33\text{ V}$ , $5\text{ mA} \leq I_{OUT} \leq 1.0\text{ A}$ , $p \leq 15\text{ W}$	17.1	18.9	18.9	V
Quiescent Current	$T_J = 25^\circ\text{C}$	4.5	8.0	8.0	mA
Quiescent Current Change	$21\text{ V} \leq V_{IN} \leq 33\text{ V}$ $5\text{ mA} \leq I_{OUT} \leq 1.0\text{ A}$	1.0	0.5	0.5	mA
Output Noise Voltage	$T_A = 25^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ kHz}$	110			μV
Long Term Stability				72	mV
Ripple Rejection	$I_{OUT} = 20\text{ mA}$ , $f = 120\text{ Hz}$	59			dB
Dropout Voltage	$I_{OUT} = 1\text{ A}$ , $T_J = 25^\circ\text{C}$	2.0			V
Output Resistance	$I_{OUT} = 500\text{ mA}$	110			mΩ
Short Circuit Current Limit	$T_J = 25^\circ\text{C}$	200			mA
Average Temperature Coefficient of Output Voltage	$I_{OUT} = 5\text{ mA}$ , $0^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$	-1.0			mV/°C

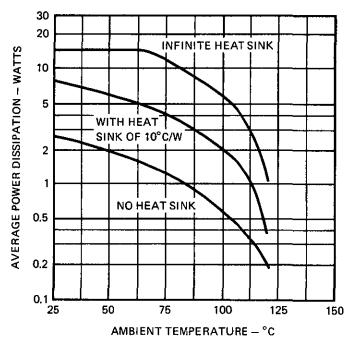
## μA7824

ELECTRICAL CHARACTERISTICS ( $V_{IN} = 33\text{ V}$ ,  $I_{OUT} = 500\text{ mA}$ ,  $0^\circ\text{C} < T_J < 125^\circ\text{C}$ , unless otherwise specified)

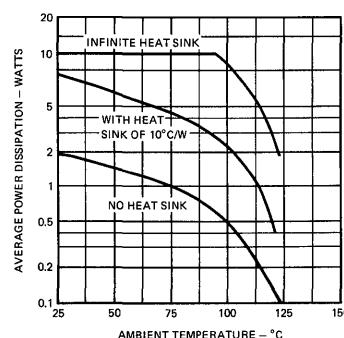
PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Output Voltage	$T_J = 25^\circ\text{C}$	23.0	24.0	35.0	V
Line Regulation	$T_J = 25^\circ\text{C}$ , $I_{OUT} = 100\text{ mA}$ $27\text{ V} \leq V_{IN} \leq 38\text{ V}$ $30\text{ V} \leq V_{IN} \leq 36\text{ V}$ $T_J = 25^\circ\text{C}$ , $I_{OUT} = 500\text{ mA}$ $27\text{ V} \leq V_{IN} \leq 38\text{ V}$ $30\text{ V} \leq V_{IN} \leq 36\text{ V}$	31	240	mV	
		14	120	mV	
Load Regulation	$T_J = 25^\circ\text{C}$ , $5\text{ mA} \leq I_{OUT} \leq 1.0\text{ A}$ $250\text{ mA} \leq I_{OUT} \leq 750\text{ mA}$	118	480	mV	
		70	240	mV	
Output Voltage	$27\text{ V} \leq V_{IN} \leq 38\text{ V}$ , $5\text{ mA} \leq I_{OUT} \leq 1.0\text{ A}$ $P \leq 15\text{ W}$	150	480	mV	
Quiescent Current	$T_J = 25^\circ\text{C}$	85	240	mV	
Quiescent Current Change	$27\text{ V} \leq V_{IN} \leq 38\text{ V}$ $5\text{ mA} \leq I_{OUT} \leq 1.0\text{ A}$	4.6	8.0	mA	
Output Noise Voltage	$T_A = 25^\circ\text{C}$ , $10\text{ Hz} \leq f \leq 100\text{ kHz}$	1.0	0.5	mA	
Long Term Stability	$I_{OUT} = 20\text{ mA}$ , $f = 120\text{ Hz}$	170	96	mV	
Ripple Rejection	$I_{OUT} = 1\text{ A}$ , $T_J = 25^\circ\text{C}$	56	2.0	dB	
Dropout Voltage	$I_{OUT} = 500\text{ mA}$	150	150	mΩ	
Output Resistance	$T_J = 25^\circ\text{C}$	150	150	mA	
Short Circuit Current Limit	$I_{OUT} = 5\text{ mA}$	1.0	1.0	mV/°C	
Average Temperature Coefficient of Output Voltage	$0^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$	-1.0			

## TYPICAL PERFORMANCE CURVES

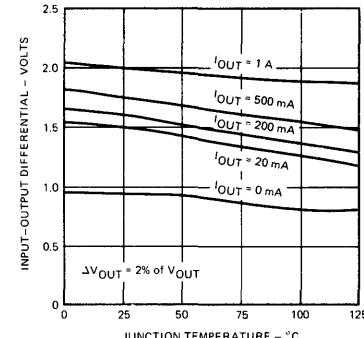
MAXIMUM AVERAGE POWER DISSIPATION AS A FUNCTION OF AMBIENT TEMPERATURE (TO-3 PACKAGE)



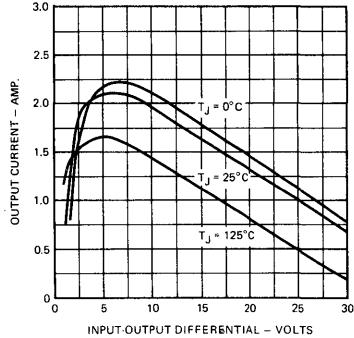
MAXIMUM AVERAGE POWER DISSIPATION AS A FUNCTION OF AMBIENT TEMPERATURE (TO-220 PACKAGE)



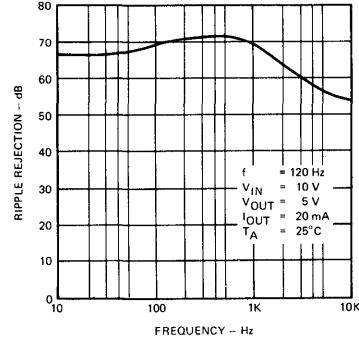
DROPOUT VOLTAGE AS A FUNCTION OF JUNCTION TEMPERATURE



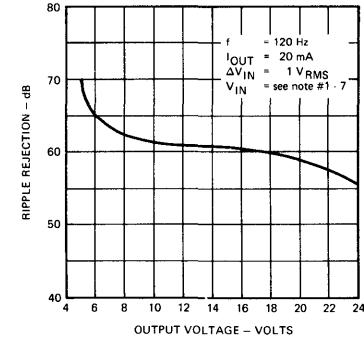
PEAK OUTPUT CURRENT AS A FUNCTION OF INPUT-OUTPUT DIFFERENTIAL VOLTAGE



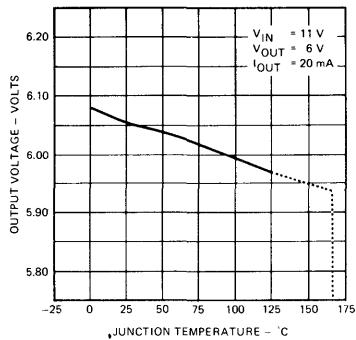
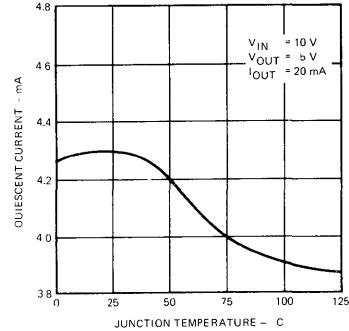
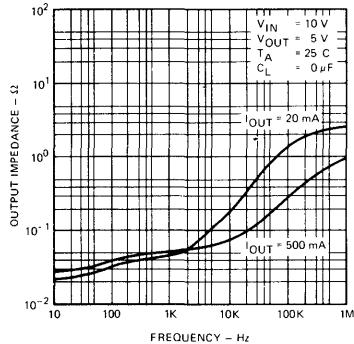
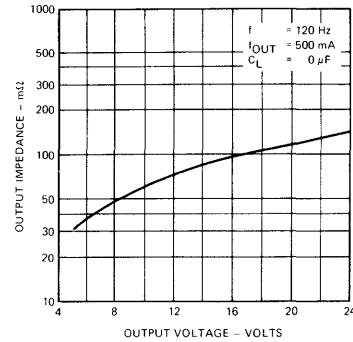
RIPPLE REJECTION AS A FUNCTION OF FREQUENCY



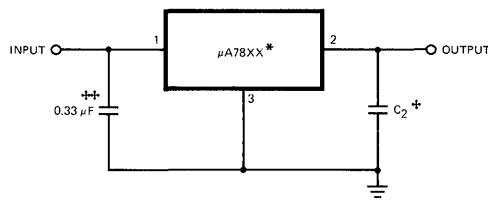
RIPPLE REJECTION AS A FUNCTION OF OUTPUT VOLTAGES



## TYPICAL PERFORMANCE CURVES (cont'd)

OUTPUT VOLTAGE  
AS A FUNCTION OF JUNCTION TEMPERATUREQUIESCENT CURRENT  
AS A FUNCTION OF TEMPERATUREOUTPUT IMPEDANCE  
AS A FUNCTION OF FREQUENCYOUTPUT IMPEDANCE  
AS A FUNCTION OF OUTPUT VOLTAGE

## APPLICATIONS



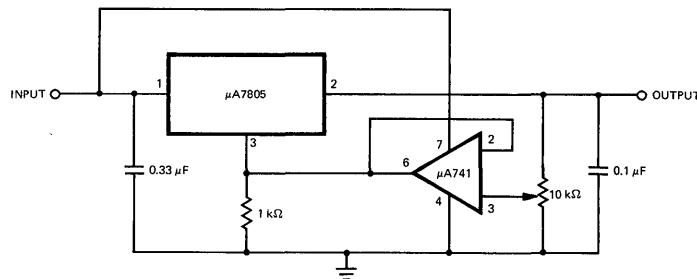
## NOTES:

\*To specify an output voltage, substitute voltage value for "XX".

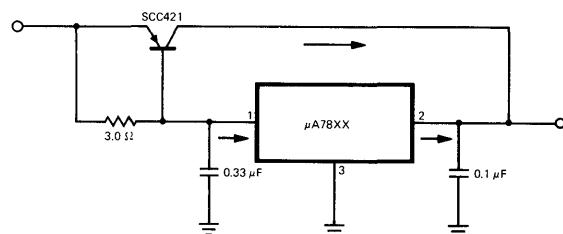
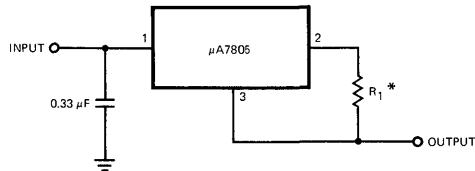
†Although no output capacitor is needed for stability, it does improve transient response.

++ Required if regulator is located an appreciable distance from power supply filter.

## FIXED OUTPUT REGULATOR

 $V_{OUT}$ , 7 V to 20 V $V_{IN} - V_{OUT} \geq 2 V$ 

## ADJUSTABLE OUTPUT REGULATOR – HIGH LINE REGULATION

\*  $R_1$  determines output current.

CURRENT REGULATOR

HIGH CURRENT VOLTAGE REGULATOR